81751.0066 Express Mail Label No. EV 325 217 457 US

Hirosh Serial Filed: For:	pplication of: ii MASUYA of Yamagata-ke No: Not assigned September 17, 2003 Semiconductor Device , and Electronic Instrument		ıfactu	ring the Same, Circu	it			2264 U.S. PTC 10/664585	
Comm P.O. E	top PATENT APPLICATION hissioner for Patents lox 1450 ndria, VA 22313-1450 Sir:	I						Ñ	
	ESPONDENCE ADDRESS	: .	⊠ Cust	omer Number 000026	6021				
Transmitted herewith for filing is the patent application identified above.									
		CAI TOTAL NO. OF	CULA	TION OF FEES NO. OF CLAIMS	LG/S	SM	I		
-	ITEM	CLAIMS		OVER BASE	\$ ENTIT	Y FEE	\$ AMOUNT	\$ FEE	
A	TOTAL CLAIMS FE E	20	-20 -3	0	SM=\$9 LG=\$84	\$18	\$ 0		
С	INDEPENDENT CLAIMS FEE*	<u> </u>		-	SM=\$42	\$84	\$ 0	0	
D	SUBTOTAL - ADDITIONAL CLAIM MULTIPLE-DEPENDENT CLAIMS		OMIN IN	LINES A + B)	LAF	GE ENTI	TY FEE = \$280	\$ 0	
							TY FEE = \$140		
E BASIC FEE LARGE ENTITY FEE = \$750 SMALL ENTITY FEE = \$375 \$ 75							\$ 750		
	F TOTAL FILING FEE (ADD TOTALS FOR LINES C, D, AND E) G ASSIGNMENT RECORDING FEE \$40						\$ 750 \$ 40		
	*LIST INDEPENDENT CLAIMS 1 and 12.								
 Please charge my Deposit Account No. 50-1314 the amount of \$_0 A copy of this letter is enclosed. A check in the amount of \$_0_ to cover the filing fee is enclosed. A check in the amount of \$_40.00_ to cover Assignment Recordation fee is enclosed. The Commissioner is hereby authorized to charge any deficiency for the following fees associated with this communication or credit any overpayment to Deposit Account No. 50-1314. A copy of this sheet is enclosed. Any additional filing fees required under 37 C.F.R. 1.16 Any patent application processing fees under 37 C.F.R. 1.17 									
				Respectfully sub HOGAN & HAB	omitted./				
Date: September 17, 2003				By:	yyv		•		
		Anthorny J. (Registration Attorney for	No 641,2						

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No: 81751.0066

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:	Art Unit:	Not assigned
Hiroshi MASUYA	Examiner:	Not assigned
Serial No: Not assigned		
Filed: September 17, 2003		
For: Semiconductor Device and Method of Manufacturing the Same, Circuit Board, and Electronic Instrument		

"Express Mail" Mailing Label No. EV 325 217 457 US
Date of Deposit: September 17, 2003

Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

I hereby certify that

- two copies of a letter of transmittal
- patent application (<u>17</u> page(s) of specification; <u>20</u> claim(s); <u>1</u> page(s) of abstract
- certified copy of <u>Japanese</u> patent application No. <u>2002-281082</u> which was filed <u>September 26, 2002</u> from which priority is claimed in the subject case pursuant to 35 U.S.C. § 119
- return postcard

are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service with sufficient postage under 37 C.F.R. § 1.10 on the date indicated above and are addressed to:

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Date: September 17, 2003

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Name of person making papers

Signature